RoHS

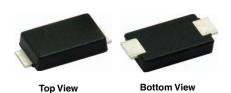
HALOGEN FREE



Vishay General Semiconductor

Surface Mount Trench MOS Barrier Schottky Rectifier

TMBS® SlimSMATM



DO-221AC

PRIMARY CHARACTERISTICS			
I _{F(AV)}	3.0 A		
V _{RRM}	50 V		
I _{FSM}	80 A		
V _F at I _F = 3.0 A	0.40 V		
T _J max.	150 °C		
Package	DO-221AC (SlimSMA)		
Diode variation	Single die		

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

FEATURES

- Very low profile typical height of 0.95 mm
- · Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

MECHANICAL DATA

Case: DO-221AC (SlimSMA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD22-B102

M3 suffix meets JESD 201 class 2 whisker test Polarity: Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSSAF3N50	UNIT	
Device marking code		3N5		
Maximum repetitive peak reverse voltage	V _{RRM}	50	V	
Maximum DC forward current (fig. 1)	I _F ⁽¹⁾	3.0	Α	
	I _F ⁽²⁾	2.7		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	80	А	
Maximum DC reserve voltage	V _{DC}	35	V	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Note

- (1) Mounted on 5 mm x 5 mm copper pad areas, 2 oz. FR4 PCB
- (2) Free air, mounted on recommended copper pad area



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 1.5 A	- T _A = 25 °C	V _E (1)	0.40	-	V	
	$I_F = 3.0 \text{ A}$			0.47	0.54		
	I _F = 1.5 A	T _A = 125 °C	1 ' 1	VF ('')	0.30	-	V
	$I_F = 3.0 \text{ A}$			0.40	0.48]	
Reverse current	V _R = 35 V	T _A = 25 °C T _A = 125 °C	I _R ⁽²⁾	0.01	-		
	v _R = 35 v			8	-	mA	
	V _R = 50 V	T _A = 25 °C		-	1	IIIA	
	v _R = 50 v	T _A = 125 °C		12.5	35		
Typical junction capacitance	4.0 V, 1 MHz		CJ	570	-	pF	

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width $\leq 5 \text{ ms}$

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise specified)				
PARAMETER SYMBOL VSSAF3N50				
Typical thermal registeres	R _{0JA} (1)	115	°C/W	
Typical thermal resistance	R _{0JM} (1)	12] C/VV	

Note

 $^{(1)}$ Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance $R_{\theta JA}$ - junction to ambient, $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	BASE QUANTITY	DELIVERY MODE		
VSSAF3N50-M3/6A	0.032	6A	3500	7" diameter plastic tape and reel	
VSSAF3N50-M3/6B	0.032	6B	14 000	13" diameter plastic tape and reel	

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

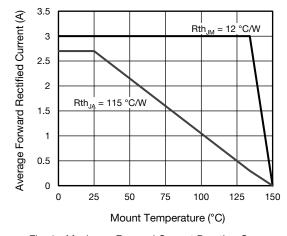


Fig. 1 - Maximum Forward Current Derating Curve

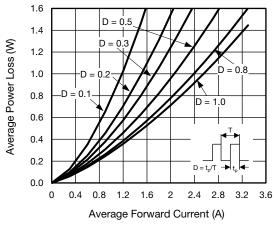


Fig. 2 - Forward Power Loss Characteristics



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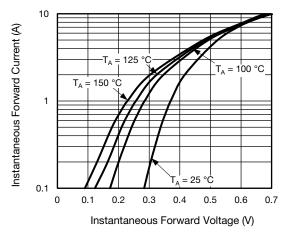


Fig. 3 - Typical Instantaneous Forward Characteristics

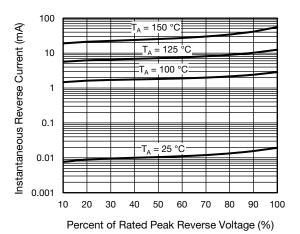


Fig. 4 - Typical Reverse Leakage Characteristics

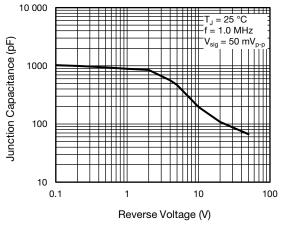


Fig. 5 - Typical Junction Capacitance

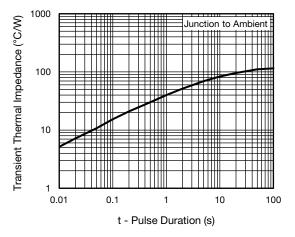


Fig. 6 - Typical Transient Thermal Impedance

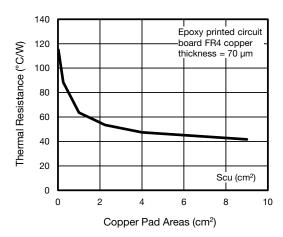


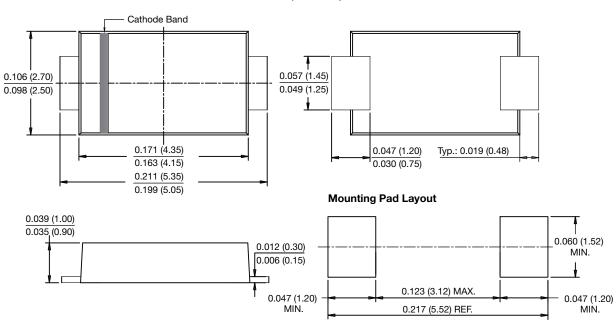
Fig. 7 - Thermal Resistance Junction to Ambient vs. Copper Pad Area



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

DO-221AC (SlimSMA)





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