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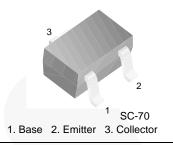
June 2013



FJX3906 PNP Epitaxial Silicon Transistor

Feature

• General-Purpose Transistor



Ordering Information

| Part Number | Top Mark | Package | Packing Method |
|-------------|----------|---------|----------------|
| FJX3906TF | S2A | SC70 3L | Tape and Reel |

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

| Symbol | Parameter | Value | Units |
|------------------|-----------------------------|-------------|-------|
| V _{CBO} | Collector-Base Voltage | -40 | V |
| V _{CES} | Collector-Emitter Voltage | 40 | V |
| V _{EBO} | Emitter-Base Voltage | -5 | V |
| ۱ _C | Collector Current | -200 | mA |
| P _C | Collector Power Dissipation | 350 | mW |
| T _{STG} | Storage Temperature | -55 to +150 | °C |

Thermal Characteristics⁽¹⁾

| Symbol | Parameter | Value | Unit |
|-----------------------|-------------------------------------|-------|-------|
| PD | Derate above 25°C | 2.8 | mW/°C |
| $R_{	extsf{	heta}JA}$ | Thermal Resistance, Junction to Air | 357 | °C/W |

Note:

1. PCB size: FR-4 76 x 114 x 0.6 T mm³ (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

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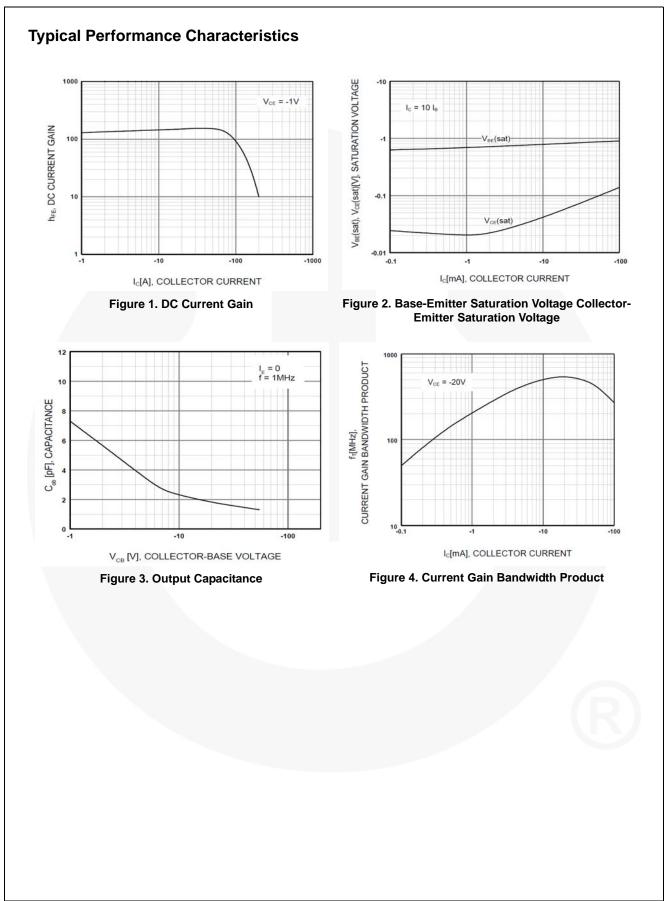
Electrical Characteristics⁽²⁾

Values are at T_A = 25°C unless otherwise noted.

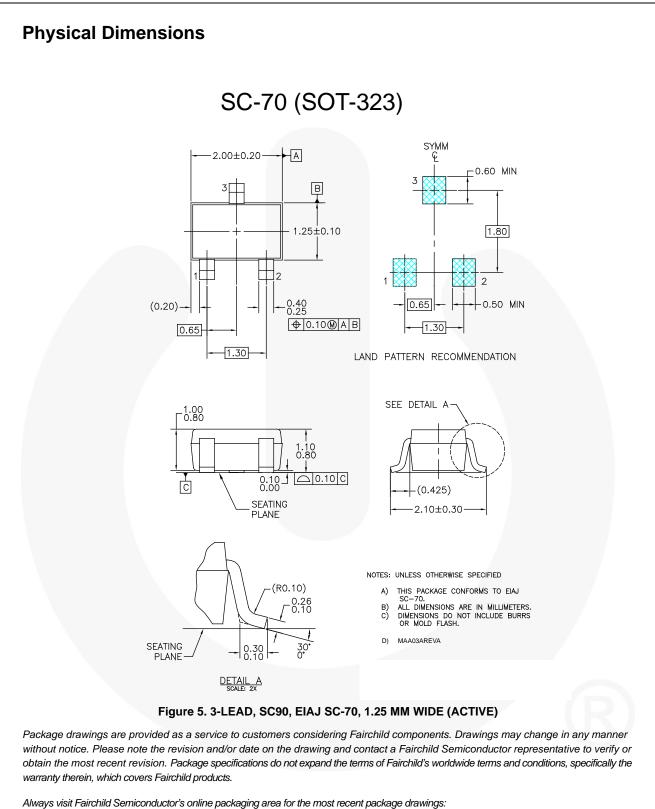
| Symbol | Parameter | Test Conditions | Min. | Max. | Units |
|-----------------------|--------------------------------------|---|-------|-------|-------|
| BV _{CBO} | Collector-Base Breakdown Voltage | $I_{C} = -10 \ \mu A, I_{E} = 0$ | -40 | | V |
| BV _{CEO} | Collector-Emitter Breakdown Voltage | I _C = -1.0 mA, I _B = 0 | -40 | | V |
| BV _{EBO} | Emitter-Base Breakdown Voltage | $I_{E} = 10 \ \mu A, I_{C} = 0$ | -5 | | V |
| I _{CEX} | Collector Cut-Off Current | $V_{CE} = -30 \text{ V}, \text{ V}_{EB} = -3 \text{ V}$ | | -50 | nA |
| h _{FE} | DC Current Gain | $V_{CE} = -1 V, I_{C} = -0.1 mA$ | 60 | | |
| | | $V_{CE} = -1 V, I_{C} = -1 mA$ | 80 | | |
| | | $V_{CE} = -1 V, I_{C} = -10 mA$ | 100 | 300 | |
| | | V _{CE} = -1 V, I _C = -50 mA | 60 | | |
| | | V _{CE} = -1 V, I _C = -100 mA | 30 | | |
| V _{CE} (sat) | Collector-Emitter Saturation Voltage | I _C = -10 mA, I _B = -1 mA | | -0.25 | V |
| | | I _C = -50 mA, I _B = -5 mA | | -0.40 | V |
| V _{BE} (sat) | Base-Emitter Saturation Voltage | I _C = -10 mA, I _B = -1 mA | -0.65 | -0.85 | V |
| | | I _C = -50 mA, I _B = -5 mA | | -0.95 | V |
| C _{ob} | Output Capacitance | $V_{CB} = -5 V$, $I_E = 0$, $f = 1 MHz$ | | 4.5 | pF |
| f _T | Current Gain Bandwidth Product | $V_{CE} = -20$ V, $I_{C} = -10$ mA | 250 | | MHz |
| NF | Noise Figure | I_{C} = -10 μA, V _{CE} = -5 V, R _S = 1 kΩ, f = 10 Hz to 15.7 kHz | | 4 | dB |
| t _{ON} | Turn-On Time | $V_{CC} = -3 \text{ V}, V_{BE} = -0.5 \text{ V},$ $I_{C} = -10 \text{ mA}, I_{B1} = -1 \text{ mA}$ | | 70 | ns |
| t _{OFF} | Turn-Off Time | $V_{CC} = -3 V, I_C = -10 mA,$ $I_{B1} = I_{B2} = 1 mA$ | | 300 | ns |

Note:

2. Pulse test: pulse width \leq 300 μ s, duty cycle \leq 2.0%.



FJX3906 — PNP Epitaxial Silicon Transistor



For current tape and reel specifications, visit Fairchild Semiconductor's online packaging area: <u>http://www.fairchildsemi.com/packaging/tr/sc703_tr.pdf</u>.

FJX3906 —

PNP Epitaxial Silicon Transistor

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| Datasheet Identification | Product Status | Definition |
|--------------------------|-----------------------|--|
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| Preliminary | First Production | Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design. |
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